Issue:
If the input voltage of the PDU drops below ~11V, e.g. QP1 on Schematic LAT-DS-02127 turns partially off and the power dissipated in the device exceeds the rating and may burn up.
The Gate-Source voltage must at least be 4V according to the data-sheet of the MOSFET transistor. That voltage is derived from the voltage divider R31/R10 (8.25kohm/13kohm). Thus the input voltage must be at least 10.3 V to ensure that the device is completely on when the power is enabled.