

TPS Subassembly Electrical Interface Continuity and Isolation Test

COVER SHEET

Program: GLAST

Procedure Number: LAT-DS-04099

Procedure Title: TPS EICIT

Paragraph Number: 5.1.3

Paragraph Title: Test Equipment

Controlling Document Number: _____


Controlling Document Step Number: _____

Unit S/N: 3

Descriptive Comment: Third TPS Flight board

TEST READINESS REVIEW COMPLETED AND APPROVED BY THE FOLLOWING:


Test Director: [Signature] Date: 12/14/04

Quality Assurance: [Signature]  Date: 12.14.04

Test Conductor: [Signature] Date: 12/14/04

POST TEST REVIEW COMPLETED AND APPROVED BY THE FOLLOWING:


Test Director: [Signature] Date: 12/14/04

Quality Assurance: [Signature]  Date: 12.14.04

Test Conductor: [Signature] Date: 12/14/04

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TPS Subassembly Electrical Interface Continuity and Isolation Test

TEST DATA SHEET		Unit S/N: 3	Date/Temperature: 12/14/04 RT	
Title: 5.13 Test Equipment		Operator: JL	QA: 	
Para./ Step	Test Equipment Description and Manufacturer	Model/LAT Number	Serial/Rev. Number	*Cal./Val. Date
5.1.3 - 1	Record Model/LAT number, Serial/Revision number, Calibration due dates and Validation date for all equipment used in this procedure:			
	Breakout Box Assembly, 78-Pin, SLAC	LAT-DS-03580	GLAT 1388	9/05
	Breakout Box Assembly, 26-Pin SLAC,	LAT-DS-03578	GLAT 1386	9/05
	BOB adapter cable (26 pin), SLAC	LAT-DS-04299	GLAT 1427	9/05
	BOB adapter cable (26 pin), SLAC	LAT-DS-04298	GLAT 1425	9/05
	BOB adapter cable (78 pin), SLAC	LAT-DS-04302	GLAT 1400	9/05
	Digital Multimeter, Fluke	87 III	GLAT 1667	11.3.05

* This column is for recording the calibration due date for a given piece of equipment or the date that EGSE was validated.

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COVER SHEET

Program: GLAST

Procedure Number: LAT-DS-04099

Procedure Title: TPS EICIT

Paragraph Number: 5.1.4

Paragraph Title: Participant List


Controlling Document Number: _____

Controlling Document Step Number: _____


Unit S/N: 3

Descriptive Comment: _____

TEST READINESS REVIEW COMPLETED AND APPROVED BY THE FOLLOWING:


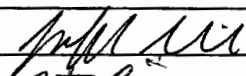
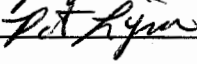
Test Director: [Signature] Date: 12/14/04
Quality Assurance: [Signature]  Date: 12.14.04
Test Conductor: [Signature] Date: 12/14/04

POST TEST REVIEW COMPLETED AND APPROVED BY THE FOLLOWING:

Test Director: [Signature] Date: 12/14/04
Quality Assurance: [Signature]  Date: 12/14/04
Test Conductor: [Signature] Date: 12/14/04

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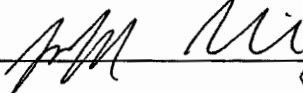


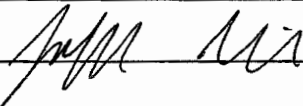
TEST DATA SHEET		Unit S/N: 3	Date/Temperature: 12/14/04 RT
Title: 5.14 Participant List		Operator: JL	QA: 
Para./ Step	Title	Print Name	Signature
5.1.4 - 1	Record names of all personnel that take part in the test/operation:		
	Engineer	Jeffrey Ludvik	
	CRE	PAT LUJAN	

TPS Subassembly Electrical Interface Continuity and Isolation Test

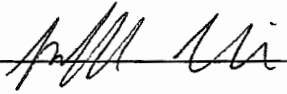


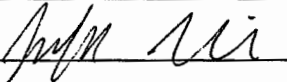
COVER SHEET

Program: GLAST
Procedure Number: LA-TD-04099
Procedure Title: TPS EICIT
Paragraph Number: 5.2
Paragraph Title: EICIT Testing
Unit S/N: 3

TEST READINESS REVIEW COMPLETED AND APPROVED BY THE FOLLOWING:


Test Director:  Date: 12/14/04
Quality Assurance:   Date: 12.14.04
Test Conductor:  Date: 12/14/04

REVIEWED AND APPROVED BY THE FOLLOWING:

Test Director:  Date: 12/14/04
Quality Assurance:   Date: 12.14.04
Test Conductor:  Date: 12/14/04


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TEST DATA SHEET		Unit S/N: 3	Date: 12/14/04	
Title: 5.2 EICIT Testing		Operator: DL	QA: 	
Para./ Step	Description	Limits	Unit	Data
5.2.1	Pre-Operation Verifications			
-1	Notify QAE.	OK	OK/NG	OK
-2	Test Readiness Review is done.	OK	OK/NG	OK
-3	Record the EUT equipment:			
	Part number	NA	NA	LAT-DS-01482
	Serial number	NA	NA	3
-4	Power to the LAT or EGSE is off.	OFF	ON/OFF	OFF
-5	Set DMM to autoranging for resistance.	OK	OK/NG	OK
-6	Measure DMM lead resistance.	< 2.0	Ω	0.1
-8	Remove all shorting plugs from BOBs.	OK	OK/NG	OK
-9	Measure BOB to ground.	< 2.0	Ω	0.3
-11	Measure EUT to ground.	< 2.0	Ω	0.6
-12	Measure test equipment to ground.	< 2.0	Ω	0.3
-13	All connector savers are installed on the flight connections.	OK	OK/NG	OK
-14	The test equipment and participant lists have been completed.	OK	OK/NG	OK

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TEST DATA SHEET		Unit S/N: 3	Date: 12/14/04		
Title: 5.2 EICIT Testing		Operator: JL	QA: 		
Para.	Description	Limits	Unit	Data	
5.2.2	J2 Connection Checks				
-1	Setup is complete.	OK	OK/NG	OK	


TEST DATA SHEET		Unit S/N: 3	Date: 12.14.04			
Title: 5.2 EICIT Testing		Operator: JL	QA: 			
Para./ Step	Signal Pair Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.2.1 -1	J2 - Continuity, TPS side of the interface					
	Chassis GND	26	BOB GND	<2	Ω	
	TEM_28V	1	2	<2	Ω	0.6
	TEM_28V	1	3	<2	Ω	0.6
	TEM_28V	1	4	<2	Ω	0.7
	28VRET	5	6	<2	Ω	0.8
	28VRET	5	7	<2	Ω	0.6
	28VRET	5	8	<2	Ω	0.6
	TEM_V1_N	5	9	<2	Ω	0.7
	TEM_V2_N	5	20	<2	Ω	0.7
	GND	5	26	<2	Ω	

JSL 12/14/04
N/A

JSL 12/14/04
N/A

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TEST DATA SHEET			Unit S/N: 3		Date: 12/14/04	
Title: 5.2 EICIT Testing			Operator: JL		QA: 	
Para.	Signal Pair Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.2.2 -1	J2 - Continuity, PDU side of the interface					
	Chassis GND	26	BOB GND	<2	Ω	skip
	TEM_28V	1	2	<2	Ω	↓
	TEM_28V	1	3	<2	Ω	
	TEM_28V	1	4	<2	Ω	
	28VRET	5	6	<2	Ω	
	28VRET	5	7	<2	Ω	
	28VRET	5	8	<2	Ω	
	TEM_V1_N	5	9	<2	Ω	
	TEM_V2_N	5	20	<2	Ω	
GND	5	26	<2	Ω		

TEST DATA SHEET			Unit S/N: 3		Date: 12/14/04		
Title: 5.2 EICIT Testing			Operator: JL		QA:		
Para.	Signal 1 Name	Signal 2 Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.2.3 -1	J2 - Power and Ground Isolation , TPS side of interface						
	TEM_28V	28VRET	J2-1	J2-5	> 1 k	Ω	34K
5.2.2.4 -1	J2 - Power and Ground Isolation, PDU side of interface						
	TEM_28V	28VRET	J2-1	J2-5	> 1 k	Ω	skip


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TEST DATA SHEET		Unit S/N: 3			Date: 12/14/04		
Title: 5.2 EICIT Testing		Operator: JL			QA. 		
Para./ Step	Signal 1 Name	Signal 2 Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.2.5	J2 - Signal Isolation from Power and Ground, TPS side of interface						
-1	Test leads at pins 1 and 5, for common connection				OK	OK/NG	OK
-2	TEM_28V	TEM_PS_T1_P	1	10	> 10 k	Ω	open
	TEM_28V	TEM_PS_T1_N	1	11	> 10 k	Ω	open
	TEM_28V	TEM_PS_T2_P	1	12	> 10 k	Ω	open
	TEM_28V	TEM_PS_T2_N	1	13	> 10 k	Ω	open
	TEM_28V	TEM_T1_P	1	14	> 10 k	Ω	open
	TEM_28V	TEM_T1_N	1	15	> 10 k	Ω	open
	TEM_28V	TEM_T2_P	1	16	> 10 k	Ω	open
	TEM_28V	TEM_T2_N	1	17	> 10 k	Ω	open
	TEM_28V	TEM_V1_P	1	18	> 10 k	Ω	open
	TEM_28V	TEM_V2_P	1	19	> 10 k	Ω	open
	TEM_28V	Not Used	1	21	> 1 k	Ω	open
	TEM_28V	Not Used	1	22	> 1 k	Ω	open
	TEM_28V	Not Used	1	23	> 1 k	Ω	open
	TEM_28V	Not Used	1	24	> 1 k	Ω	open
TEM_28V	Not Used	1	25	> 1 k	Ω	open	


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TEST DATA SHEET		Unit S/N: 3		Date: 12/14/04			
Title: 5.2 EICIT Testing		Operator: JL		QA: 			
Para./ Step	Signal 1 Name	Signal 2 Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.2.6	J2 - Signal Isolation from Power and Ground, PDU side of interface						
-1	Test leads at pins 1 and 5, for common connection				OK	OK/NG	skip
-2	TEM_28V	TEM_PS_T1_P	1	10	> 10 k	Ω	↓
	TEM_28V	TEM_PS_T1_N	1	11	> 10 k	Ω	
	TEM_28V	TEM_PS_T2_P	1	12	> 10 k	Ω	
	TEM_28V	TEM_PS_T2_N	1	13	> 10 k	Ω	
	TEM_28V	TEM_T1_P	1	14	> 10 k	Ω	
	TEM_28V	TEM_T1_N	1	15	> 10 k	Ω	
	TEM_28V	TEM_T2_P	1	16	> 10 k	Ω	
	TEM_28V	TEM_T2_N	1	17	> 10 k	Ω	
	TEM_28V	TEM_V1_P	1	18	> 10 k	Ω	
	TEM_28V	TEM_V2_P	1	19	> 10 k	Ω	
	TEM_28V	Not Used	1	21	> 1 k	Ω	
	TEM_28V	Not Used	1	22	> 1 k	Ω	
	TEM_28V	Not Used	1	23	> 1 k	Ω	
	TEM_28V	Not Used	1	24	> 1 k	Ω	
TEM_28V	Not Used	1	25	> 1 k	Ω		

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TEST DATA SHEET		Unit S/N: 3	Date: 12/14/04	
Title: 5.2 EICIT Testing		Operator: JL	QA: 	
Para.	Description	Limits	Unit	Data
5.2.3	J1 - Connector Checks			
5.2.3 -1	Setup is complete.	OK	OK/NG	OK

TEST DATA SHEET		Unit S/N: 3		Date: 12/14/04		
Title: 5.2 EICIT Testing		Operator: JL		QA: 		
Para./ Step	Signal Pair Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.3.1 -1	J1 - Continuity, TPS side of the interface					
	TEM 2.5 V Supply	1	2	<2	Ω	0.8
	TEM 2.5 V Supply	1	3	<2	Ω	0.8
	TRK DIGITAL 2.5 V Supply	4	19	<2	Ω	1.0
	TRK DIGITAL 2.5 V Supply	4	20	<2	Ω	0.8
	TRK DIGITAL 2.5 V Supply	4	39	<2	Ω	1.0
	CAL DIGITAL 3.3 V Supply	5	11	<2	Ω	0.9
	CAL DIGITAL 3.3 V Supply	5	30	<2	Ω	0.9
	CAL DIGITAL 3.3 V Supply	5	50	<2	Ω	0.9
	TEM+3.3V Supply	6	45	<2	Ω	1.0
	TEM+3.3V Supply	6	65	<2	Ω	1.0
	CAL BIAS	12	23	<2	Ω	0.9
	CAL BIAS	12	24	<2	Ω	1.0
	CAL BIAS	12	31	<2	Ω	1.0
	TRK Supply ON/11/OFF/01	7	26	<2	Ω	0.9


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TEST DATA SHEET		Unit S/N: 3		Date: 12/14/04		
Title: 5.2 EICIT Testing		Operator: JL		QA: 		
Para./ Step	Signal Pair Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.3.1 -1	J1 - Continuity, TPS side of the interface (continued)					
	CAL Supply ON/11/OFF/01	46	66	<2	Ω	0.7
	TRK Bias set point	18	38	<2	Ω	0.9
	CAL Bias set point	13	32	<2	Ω	0.9
	TRK ANALOG 2.5 V SUPPLY	25	36	<2	Ω	0.8
	TRK ANALOG 2.5 V SUPPLY	25	55	<2	Ω	0.8
	TRK ANALOG 2.5 V SUPPLY	25	56	<2	Ω	0.8
	TRK BIAS	41	42	<2	Ω	0.8
	TRK BIAS	41	61	<2	Ω	0.7
	TRK BIAS	41	62	<2	Ω	0.8
	TRK BIAS	41	63	<2	Ω	0.8
	TRK BIAS	41	64	<2	Ω	0.8
	TRK BIAS	41	57	<2	Ω	0.9
	TRK BIAS	41	77	<2	Ω	0.8
	CAL ANALOG 3.3 V SUPPLY	43	52	<2	Ω	LO
	CAL ANALOG 3.3 V SUPPLY	43	71	<2	Ω	0.9
	CAL ANALOG 3.3 V SUPPLY	43	72	<2	Ω	0.8
	TRK ANALOG 1.5 V SUPPLY	44	54	<2	Ω	0.8
	TRK ANALOG 1.5 V SUPPLY	44	73	<2	Ω	0.8
	TRK ANALOG 1.5 V SUPPLY	44	74	<2	Ω	0.8


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TEST DATA SHEET		Unit S/N: 3		Date: 12/14/04		
Title: 5.2 EICIT Testing		Operator: JL		QA: 		
Para./ Step	Signal Pair Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.3.1 -1	J1 - Continuity, TPS side of the interface (continued)					
	TEM GND	8	14	<2	Ω	0.8
	TEM GND	8	15	<2	Ω	0.8
	TEM GND	8	16	<2	Ω	0.8
	TEM GND	8	17	<2	Ω	0.9
	TEM GND	8	27	<2	Ω	0.9
	TEM GND	8	33	<2	Ω	0.7
	TEM GND	8	34	<2	Ω	0.8
	TEM GND	8	35	<2	Ω	0.8
	TEM GND	8	37	<2	Ω	0.8
	TEM GND	8	47	<2	Ω	0.8
	TEM GND	8	49	<2	Ω	0.8
	TEM GND	8	51	<2	Ω	0.9
	TEM GND	8	53	<2	Ω	0.7
	TEM GND	8	58	<2	Ω	0.9
	TEM GND	8	59	<2	Ω	0.7
	TEM GND	8	68	<2	Ω	0.8
	TEM GND	8	69	<2	Ω	0.8
	TEM GND	8	70	<2	Ω	0.9
	TEM GND	8	75	<2	Ω	0.9
	TEM GND	8	76	<2	Ω	0.9
	TEM GND	8	78	<2	Ω	0.9

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TPS Subassembly Electrical Interface Continuity and Isolation Test

TEST DATA SHEET		Unit S/N: JL		Date: 12/14/04			
Title: 5.2 EICIT Testing		Operator: 12/14/04		QA: 			
Para./ Step	Signal 1 Name	Signal 2 Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.3.2 -1	J1 - Power and Ground Isolation , TPS side of interface						
	TEM 2.5 V Supply	TRK DIGITAL 2.5 V Supply	1	4	>250	Ω	1.2K
	TEM 2.5 V Supply	CAL DIGITAL 3.3 V Supply	1	5	>250	Ω	1.8K
	TEM 2.5 V Supply	TEM+3.3V Supply	1	6	>250	Ω	1.5K
	TEM GND	TEM 2.5 V Supply	8	1	>250	Ω	580
	TEM 2.5 V Supply	CAL BIAS	1	12	> 200 k	Ω	420K
	TEM 2.5 V Supply	TRK ANALOG 2.5 V Supply	1	25	>250	Ω	1.5K
	TEM 2.5 V Supply	TRK BIAS	1	41	> 200 k	Ω	535K
	TEM 2.5 V Supply	CAL ANALOG 3.3 V Supply	1	43	>250	Ω	1.6K
	TEM 2.5 V Supply	TRK ANALOG 1.5 V Supply	1	44	>250	Ω	2.5K
	TRK DIGITAL 2.5 V Supply	CAL DIGITAL 3.3 V Supply	4	5	>250	Ω	1.8K
	TRK DIGITAL 2.5 V Supply	TEM+3.3V Supply	4	6	>250	Ω	1.5K
	TEM GND	TRK DIGITAL 2.5 V Supply	8	4	>250	Ω	1K
	TRK DIGITAL 2.5 V Supply	CAL BIAS	4	12	> 200 k	Ω	425K

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TEST DATA SHEET		Unit S/N: 3		Date: 12/14/04			
Title: 5.2 EICIT Testing		Operator: JL		QA:			
Para./ Step	Signal 1 Name	Signal 2 Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.3.2 -1	J1 - Power and Ground Isolation , TPS side of interface						
	TRK DIGITAL 2.5 V Supply	TRK ANALOG 2.5 V Supply	4	25	>250	Ω	1.4K
	TRK DIGITAL 2.5 V Supply	TRK BIAS	4	41	> 200 k	Ω	535K
	TRK DIGITAL 2.5 V Supply	CAL ANALOG 3.3 V Supply	4	43	>250	Ω	1.6K
	TRK DIGITAL 2.5 V Supply	TRK ANALOG 1.5 V Supply	4	44	>250	Ω	2.4K
	CAL DIGITAL 3.3 V Supply	TEM+3.3V Supply	5	6	>250	Ω	1.5K
	TEM GND	CAL DIGITAL 3.3 V Supply	8	5	>250	Ω	1.6K
	CAL DIGITAL 3.3 V Supply	CAL BIAS	5	12	> 200 k	Ω	420K
	CAL DIGITAL 3.3 V Supply	TRK ANALOG 2.5 V Supply	5	25	>250	Ω	1.5K
	CAL DIGITAL 3.3 V Supply	TRK BIAS	5	41	> 200 k	Ω	530K
	CAL DIGITAL 3.3 V Supply	CAL ANALOG 3.3 V Supply	5	43	>250	Ω	1.7K




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TEST DATA SHEET		Unit S/N: 3		Date: 12/14/04			
Title: 5.2 EICIT Testing		Operator: JL		QA: 			
Para./ Step	Signal 1 Name	Signal 2 Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.3.2 -1	J1 - Power and Ground Isolation , TPS side of interface (continued)						
	CAL DIGITAL 3.3 V Supply	TRK ANALOG 1.5 V Supply	5	44	>250	Ω	2.5K
	TEM GND	TEM+3.3V Supply	8	6	>250	Ω	1.3K
	TEM+3.3V Supply	CAL BIAS	6	12	> 200 k	Ω	418K
	TEM+3.3V Supply	TRK ANALOG 2.5 V Supply	6	25	>250	Ω	1.5K
	TEM+3.3V Supply	TRK BIAS	6	41	> 200 k	Ω	530K
	TEM+3.3V Supply	CAL ANALOG 3.3 V Supply	6	43	>250	Ω	1.7K
	TEM+3.3V Supply	TRK ANALOG 1.5 V Supply	6	44	>250	Ω	2.5K
	TEM GND	CAL BIAS	8	12	> 200 k	Ω	430K
	TEM GND	TRK ANALOG 2.5 V Supply	8	25	>250	Ω	1.25K
	TEM GND	TRK BIAS	8	41	> 200 k	Ω	540K
	TEM GND	CAL ANALOG 3.3 V Supply	8	43	>250	Ω	1.4K
	TEM GND	TRK ANALOG 1.5 V Supply	8	44	>250	Ω	2.2K
	TEM GND	TRK ANALOG 2.5 V Supply Minus	8	55	>250	Ω	1.3K


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TEST DATA SHEET			Unit S/N: 3		Date: 12/14/04		
Title: 5.2 EICIT Testing			Operator: JL		QA: 		
Para./ Step	Signal 1 Name	Signal 2 Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.3.2 -1	J1 - Power and Ground Isolation , TPS side of interface (continued)						
	TEM GND	TRK ANALOG 1.5 V Supply Plus	8	73	>250	Ω	2.4K
	CAL BIAS	TRK ANALOG 2.5 V Supply	12	25	> 200 k	Ω	660K
	CAL BIAS	TRK BIAS	12	41	> 200 k	Ω	1.2M
	CAL BIAS	CAL ANALOG 3.3 V Supply	12	43	> 200 k	Ω	666K
	CAL BIAS	TRK ANALOG 1.5 V Supply	12	44	> 200 k	Ω	660K
	TRK ANALOG 2.5 V Supply	TRK BIAS	25	41	> 200 k	Ω	540K
	TRK ANALOG 2.5 V Supply	CAL ANALOG 3.3 V Supply	25	43	>250	Ω	1.6K
	TRK ANALOG 2.5 V Supply	TRK ANALOG 1.5 V Supply	25	44	>250	Ω	2.2K
	TRK BIAS	CAL ANALOG 3.3 V Supply	41	43	>250	Ω	860K
	TRK BIAS	TRK ANALOG 1.5 V Supply	41	44	> 200 k	Ω	860K
	CAL ANALOG 3.3 V Supply	TRK ANALOG 1.5 V Supply	43	44	> 250	Ω	2.2K
	TEM 2.5 V Supply	TEM TEMP SENSOR A	1	9	> 10 k	Ω	open
	TEM 2.5 V Supply	TEM TEMP SENSOR B	1	10	> 10 k	Ω	open
	TEM 2.5 V Supply	CAL BIAS SET POINT	1	13	> 10 k	Ω	open 3.4M

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TPS Subassembly Electrical Interface Continuity and Isolation Test

TEST DATA SHEET			Unit S/N: 3		Date: 12/14/04		
Title: 5.2 EICIT Testing			Operator: JL		QA: 		
Para./ Step	Signal 1 Name	Signal 2 Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.3.3	J1 - Signal Isolation from Power and Ground, TPS side of interface						
-1	Test leads at pins 1,4,5,6,8,12,20,25,41,43,44,55,73 for common connection				OK	OK/NG	OK
	TEM 2.5 V Supply	TEM TEMP SENSOR A	1	9	> 10 k	Ω	open
	TEM 2.5 V Supply	TEM TEMP SENSOR B	1	10	> 10 k	Ω	open
	TEM 2.5 V Supply	CAL BIAS SET POINT	1	13	> 10 k	Ω	3.7M
	TEM 2.5 V Supply	TRK BIAS SET POINT	1	18	> 10 k	Ω	3.7M
	TEM 2.5 V Supply	TEM CURRENT MONITOR HIGH	1	21	> 10 k	Ω	
	TEM 2.5 V Supply	TEM CURRENT MONITOR LOW	1	22	> 10 k	Ω	
	TEM 2.5 V Supply	TEM TEMP SENSOR A	1	28	> 10 k	Ω	open
	TEM 2.5 V Supply	TEM TEMP SENSOR B	1	29	> 10 k	Ω	open
	TEM 2.5 V Supply	TRK BIAS CURRENT	1	40	> 10 k	Ω	1M
	TEM 2.5 V Supply	TEM VOLTAGE SENSE A	1	48	> 10 k	Ω	open
	TEM 2.5 V Supply	CAL BIAS CURRENT	1	60	> 10 k	Ω	1M
	TEM 2.5 V Supply	TEM Voltage Sense B	1	67	> 10 k	Ω	open

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TPS Subassembly Electrical Interface Continuity and Isolation Test

TEST DATA SHEET			Unit S/N: 3		Date: 12/14/04		
Title: 5.2 EICIT Testing			Operator: JL		QA: 		
Para./ Step	Signal 1 Name	Signal 2 Name	Pin 1 (-)	Pin 2 (+)	Limits	Unit	Data
5.2.3.4 -1	J1 - Controlled Resistances, TPS side of interface						
	TEM 2.5 V Supply	TRK supply on(1)/off(0)	1	7	800-1200	Ω	1K
	TEM 2.5 V Supply	CAL SUPPLY ON(1)/OFF(0)	1	46	800-1200	Ω	1K
	TEM 2.5 V Supply	TEM CURRENT MONITOR HIGH	1	21	6K-9K	Ω	7.7K
	TEM 2.5 V Supply	TEM CURRENT MONITOR LOW	1	22	6K-9K	Ω	7.7K

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Appendix B (Connector Mate/Demate Log)

