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Temperature: 85°C
### TEST DATA SHEET

**Paragraph: 5.4 Performance Test Procedures (continued)**

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**Unn, S/N:** C-LAT1117  
**Date:** 8/11/04  
**Operator:** J. Ludvik  
**Temperature:** 25°C

---

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Pdu TEM Voltage 1: Raw 2951 Calibrated 3.603 V

TEM: Raw 2963 Calibrated 3.617 V

Cal Digital: Raw 3074 Calibrated 3.753 V
Cal Analog: Raw 3116 Calibrated 3.197 V
Cal Bias 0: Raw 1416 Calibrated 87.11 V

Tkr Digital: Raw 3376 Calibrated 3.0 V
Tkr Analog A: Raw 2245 Calibrated 1.720 V
Tkr Analog B: Raw 2244 Calibrated 3.007 V
Tkr Bias 0: Raw 2249 Calibrated 138.6 V
Cal Bias 1: Raw 1511 Calibrated 91.41 V
Cal Bias 0: Raw 1416 Calibrated 87.33 V
Cal Bias Current: Calibrated 0.00119 A

Tkr Bias 1: Raw 2541 Calibrated 156.6 V
Tkr Bias 0: Raw 2249 Calibrated 138.6 V
Tkr Bias Current: Calibrated 0.00353 A

Tower 28V V1: Raw 3386 Calibrated 27.41 V
Tower 28V V2: Raw 3435 Calibrated 27.72 V

Tower Current Calculation: I = 67.122000*(2.5/4095)*(V2 - V1) + -0.231400
Tower Current: Calibrated 1.36674 A
Pdu TEM voltage 0 :Raw 2759 Calibrated 3.368 V
Pdu TEM voltage 1 :Raw 2796 Calibrated 3.363 V
TEM :Raw 2772 Calibrated 3.384 V
Cal Digital :Raw 2727 Calibrated 3.329 V
Cal Analog :Raw 2761 Calibrated 3.371 V
Cal Bias 0 :Raw 763 Calibrated 46.43 V
Tkr Digital :Raw 2816 Calibrated 2.373 V
Tkr Analog A :Raw 1905 Calibrated 1.460 V
Tkr Analog B :Raw 2821 Calibrated 2.583 V
Tkr Bias 0 :Raw 1132 Calibrated 69.79 V
Cal Bias 1 :Raw 805 Calibrated 49.63 V
Cal Bias 0 :Raw 753 Calibrated 46.43 V
Cal Bias Current : Calibrated 0.00062 A
Tkr Bias 1 :Raw 1279 Calibrated 78.86 V
Tkr Bias 0 :Raw 1132 Calibrated 69.79 V
Tkr Bias Current : Calibrated 0.00137 A
Tower 28V V1 :Raw 3418 Calibrated 27.58 V
Tower 28V V2 :Raw 3449 Calibrated 27.83 V
Tower Current Calculation: I = 67.1220000*(2.5/4096)*(V2 - V1) + -0.231400
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