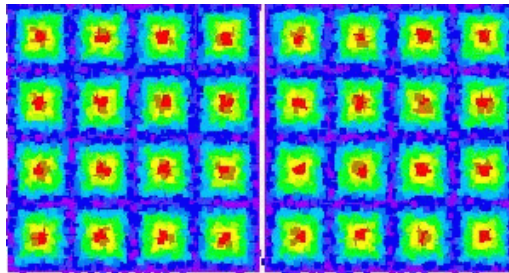


Summary and Future Plans



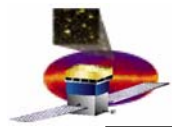
Eduardo do Couto e Silva
Instrument Analysis Workshop 1
SLAC, June 7 , 2004



Data Constraints and Idiosyncrasies

From Pat Hascall in Systems Engineering

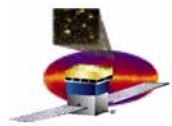
- **Data Constraints and Idiosyncrasies (LAT-TD-03775) is intended to collect LAT quirks**
 - Started by Steve Ritz
 - Goal is to identify, document and capture LAT features (that's not a bug, it's a feature), gotchas, limitations or other issues
 - The lists provided during the Cal and Tracker presentations from this morning will be added to the document
- **We need your help in documenting issues that you uncover during your test data analysis**
- **If you find any issues that should be documented in this list, please forward them to any of the following:**
 - Eduardo (he's first on the list because he's making me build this chart for his presentation)
 - Pat Hascall
 - Steve Ritz



Sample Entry (Actually the only entry)

From Pat Hascall in Systems Engineering

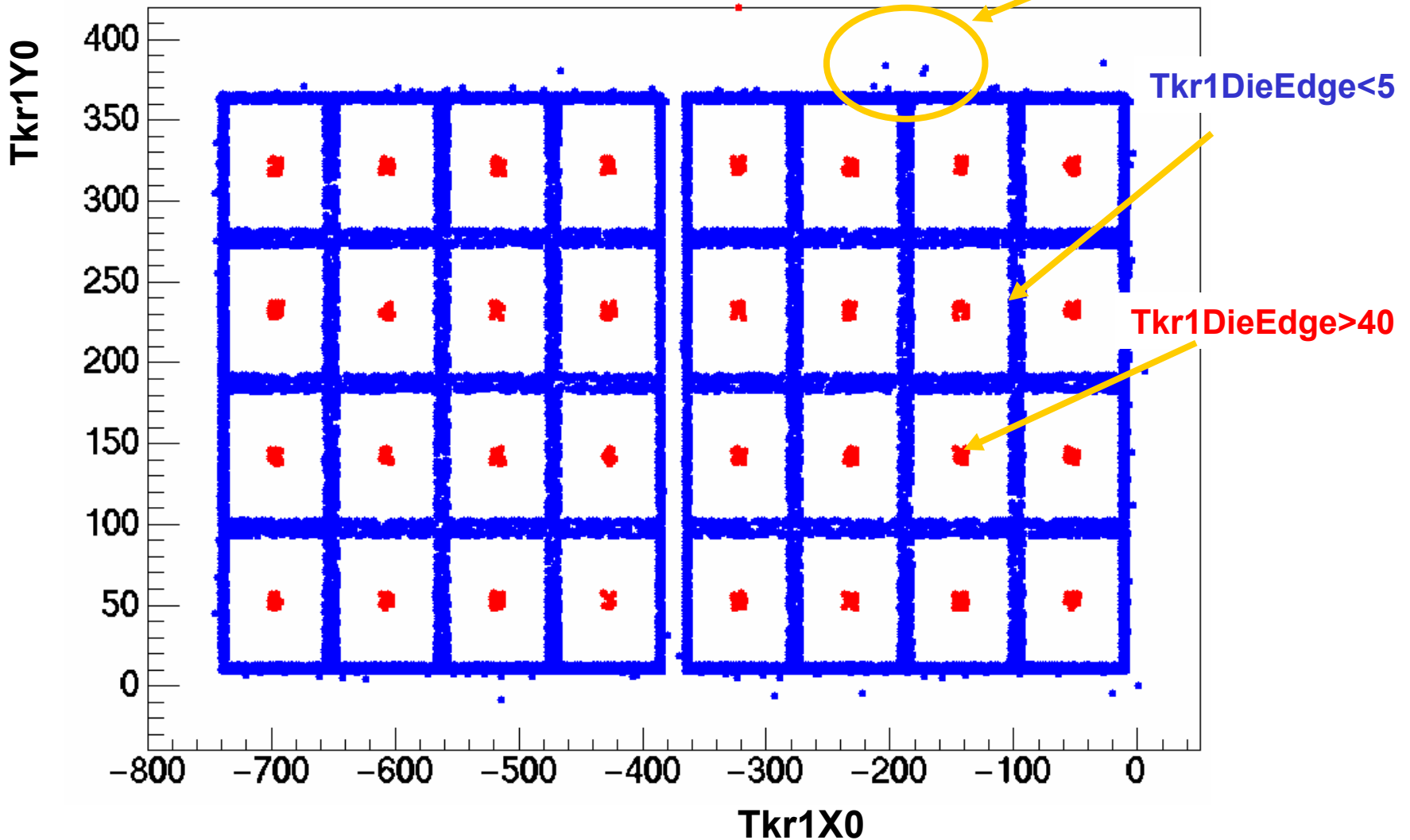
- ***Item 1: ACD GARC Header Parity Bit Design Error***
 - **One of the parity bits covering a portion of the serial data stream between the ACD FREE cards and the AEM is unusable, due to a design error. The issue and implications are described in LAT-TD-03774. Alternative data integrity checks, described in LAT-TD-03774, provide equivalent coverage. These integrity checks must be performed and monitored as part of the routine data reduction pipeline processing.**

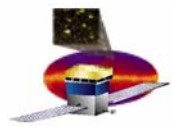


The LOGO

Tkr1DieEdge - Distance from die (wafer) edge of initial point
(0 is halfway between the dies, increases toward center of die)

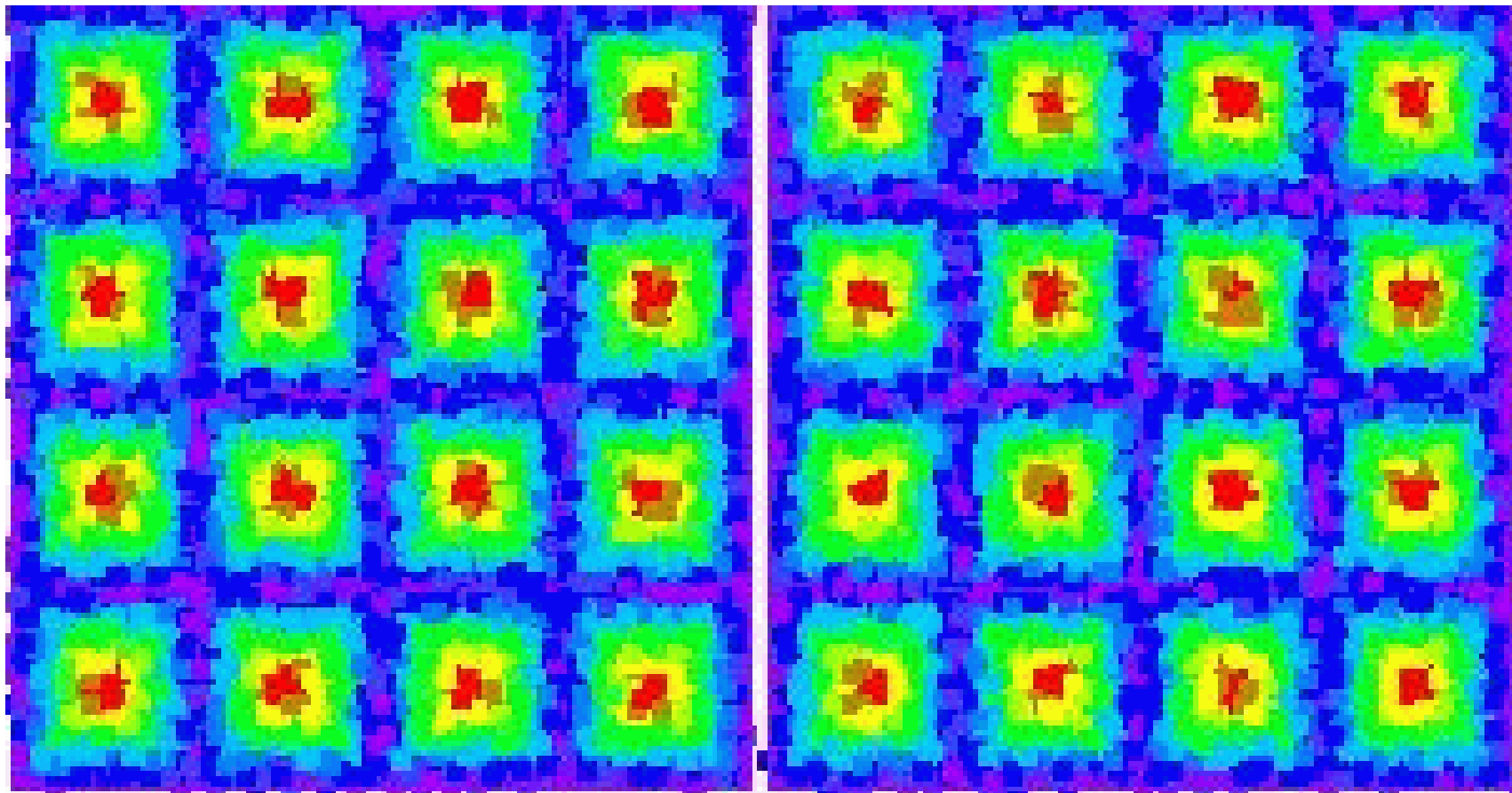
Oh no!
Another project !

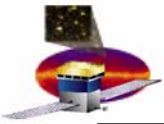




The LOGO

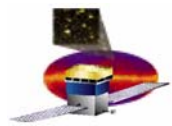
X : Tkr1X0 Y : Tkr1Y0 Z : Tkr1DieEdge





It was not easy...

- 29 people signed up for the Workshop
- 29 showed up in the dinner
 - if someone did not pay please let me know, otherwise my wife will kill me...
- ~50 to 60 attended the sessions !
 - I think it worked well, but time will tell...
- To get all of us here was a lot of work since we had to
 - Convince people that this was worthwhile
 - Work hard to get the infrastructure needed
 - Annoy people with continuous requests...



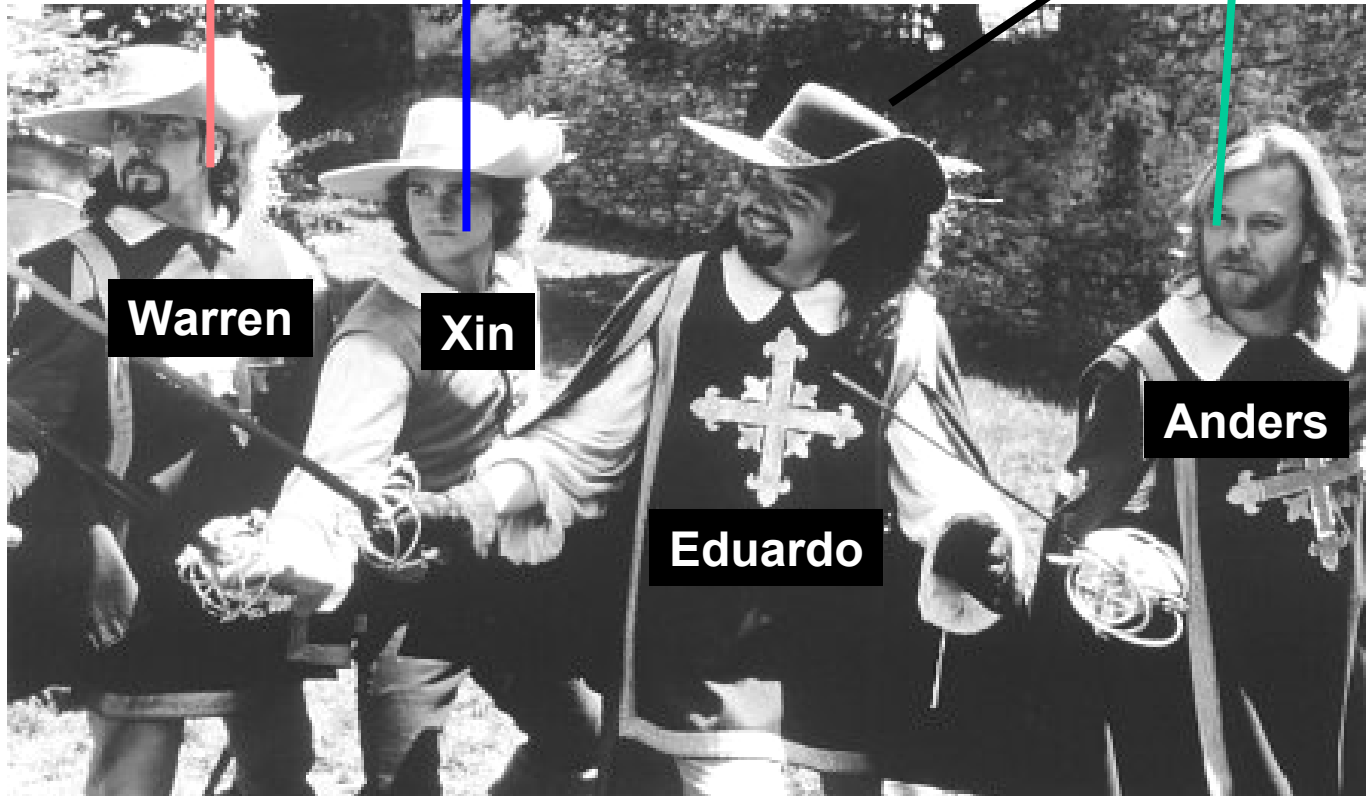
The SVAC Musketeers

C'mon Richard, this is not an unreasonable request...

No mercy,
Let's get that
pipeline!

The calibration
packages are mine !

They won't
escape from the
geometry review!



Special thanks to the three Musketeers who worked around the clock !

Meanwhile in the SAS kingdom...

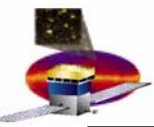
Cardinal
"Richardlieu"



Here they come again...
The I&T Musketeers want
the towers assembled
upside down with a
diagonal readout and in a
grid with a dodecahedron
configuration
Of course, due
yesterday...

The requests were
into the Gaudi
network...

Sign up ...

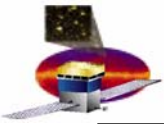


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23 entries.

**LAT instrument Analysis list
as of June 8 , 2004**

**Please sign up if your name
is not there**



What next ?

- **Project lists**
 - Discussed this afternoon we have a lot of work ahead of us
- **User Manual**
 - I would like to prepare a user manual with the info from the workshop to help you analyze data
- **Instrument Analysis VRVS meetings**
 - Weekly basis so that we can follow up
 - I will call you periodically (to see if you miss California...)
 - Biweekly reports by me in Steve's ANA meeting
 - Instrument Analysis List (IA) is set up (see next slide)
- **Working Meeting in July**
 - Core group (mostly local)
 - Evaluate where we are
 - Evaluate what to ask people to do with the EM2 data
- **Workshop 2 in September with the Collaboration Meeting**
 - 2 day workshop (TBR)
- **DATA TAKING starts in September/October !**
 - Would like to see you @ SLAC for a longer period to analyze data

Thank you for coming

Thanks for the Project Management for the Support : Peter, Steve, Lowell, Dick and Elliott
Without Diana, Debbie and Chris this could not have been so enjoyable !

<http://www-project.slac.stanford.edu/slacpix/index.htm>

