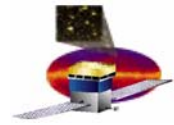


GLAST Large Area Telescope: Engineering Meeting

Lowell A. Klaisner
Stanford Linear Accelerator Center
Chief Engineer

Klaisner@slac.stanford.edu
650-967-6290



Subjects

Electronic Engineering Models and Test Bed

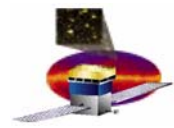
Purpose:

1. To understand each electronics plans for engineering models.
2. To review how these plans integrate into the subsystem engineering model plans.
3. To review the expected state of engineering model testing at the time of the CDR.
4. To understand the scope and schedule for the test bed.

Test Executive Trade Study

Purpose:

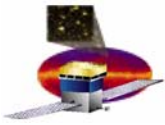
1. To review the Test Executive Trade Study findings and recommendations.
2. To understand the plan for implementation.



Agenda

Agenda:

Introduction	5 min	L. Klaisner
Electronics EMs	30 min	G. Haller
Discussion	15 min	L. Klaisner
Test Executive Trade Study	15 min	D. Lung
Discussion	5 min	D. Horn



Future Meetings

- **November 12, 2002 – No meeting, Quarterly Review**
- **November 19, 2002 – Thermal Model and Specifications**
- **November 16, 2002 – Structural Specifications**
- **December 3, 2002 – Plans and status for LAT CDR**
- **December 10, 2002 – EE Parts and M&P Qualification**
- **December 17, 2002 – TKR Final Configuration**
- **December 24, 2002 – No meeting, Holiday**
- **December 31, 2002 – No meeting, Holiday**