Tungsten and Bias Circuit Bonding

ISSUED BY: R.Bagagli

CHECKED BY: R.Bagagli

APPROVED BY: A. Brez
# Change History Log

<table>
<thead>
<tr>
<th>Revision</th>
<th>Effective Date</th>
<th>Description of Changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Sept. 13, 2004</td>
<td>Initial Release</td>
</tr>
</tbody>
</table>

Hard copies of this document are for REFERENCE ONLY and should not be considered to be the latest revision.
Table of contents

Tungsten Bonding ........................................................................................................... 1
Change History Log ........................................................................................................ 2
Table of contents............................................................................................................. 3

1 Purpose ..................................................................................................................... 4
2 Scope ........................................................................................................................ 4
3 Acronyms and Definitions ......................................................................................... 4
4 Applicable Documents .............................................................................................. 4
5 Cleanliness Requirements .......................................................................................... 5
6 Recommendation for the use of the tool.................................................................. 5
7 Procedure for Tungsten and Bias Circuit Bonding .................................................. 5
1 Purpose

The present document describes the Bonding Process of the Tungsten Tiles during the Assembly of the Tray.

2 Scope

The goal is to increase the bond adhesion, avoid contamination during the process and to maintain the tile flat.

3 Acronyms and Definitions

<table>
<thead>
<tr>
<th>Acronym</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>EM</td>
<td>Engineering Model</td>
</tr>
<tr>
<td>GLAST</td>
<td>Gamma-Ray Large Array Telescope</td>
</tr>
<tr>
<td>Plyform</td>
<td>Trays Manufacturing</td>
</tr>
<tr>
<td>MEK</td>
<td>Methyl Ethil Kethone</td>
</tr>
<tr>
<td>IPA</td>
<td>Isopropyl Alcohol</td>
</tr>
<tr>
<td>TKR</td>
<td>Tracker</td>
</tr>
<tr>
<td>TVAC</td>
<td>Thermo vacuum Test</td>
</tr>
<tr>
<td>W</td>
<td>Tungsten</td>
</tr>
<tr>
<td>3M 2216</td>
<td>3M™ Scotch-Weld™ Epoxy Adhesive 2216 B/A Gray</td>
</tr>
</tbody>
</table>

4 Applicable Documents

Documents relevant to the development of this procedure include:

[1] LAT-PS-01584, Mid Tray Assembly Procedure
5 Cleanliness Requirements

For general manufacturing guidelines and process control, refer to INFN-LAT-MECH-007-01

6 Recommendation for the use of the tool

Contamination problems should be associated with the sequential bonds to tungsten sides. Consider that mold release on alignment/bonding tool used in W bond to face sheet can be transferred to the 2\textsuperscript{nd} surface of W tile. This is a particular concern for the thin W tiles where planarity is a potential problem. With the thick tiles, planarity is very good so transfer should not be as great. \textit{For thin tiles, ensure the positioning on the tool without any friction on tooling surface.}

7 Procedure for Tungsten and Bias Circuit Bonding

1. Remove tiles with primer applied from storage bags as needed for bonding
2. Clean Tungsten tiles with Acetone and IPA as for the PROCEDURE FOR SOLVENT WIPING (INFN-LAT-MECH-007-01).
3. Prepare the face sheet surface as for Plyform I.L.
4. Prepare the glue as for Plyform I.L.
5. Degas the glue as for INFN-LAT-MECH-009-01
6. Apply the quantity of 3M 2216 Glue distribute it onto the face sheet and the Tiles as for Plyform I.L.
7. Cure 1\textsuperscript{st} bond @ room temperature (or @ 35°C), under vacuum bag for 18 hours at least.
8. 2\textsuperscript{nd} Bonding: Clean the exposed W surface with Acetone and IPA as for the PROCEDURE FOR SOLVENT WIPING (INFN-LAT-MECH-007-01). Clean the surface removing all the mold release used in 1\textsuperscript{st} bond. Use only wipes, no abrasion. The Primer on the Tiles must be protected.
9. Wait at least 1 hour for Acetone and IPA evaporation.
10. Prepare the Bias Circuit as for INFN-LAT-MECH-008-01
11. Prepare the glue as for Plyform I.L.
12. Degas the glue as for INFN-LAT-MECH-009-01
13. Apply the quantity of 3M 2216 Glue and roll it onto the face sheet, Tiles and Bias Circuit bond surface as for Plyform I.L.
14. Cure 2\textsuperscript{nd} bond @ room temperature (or @ 35°C), under vacuum bag at least for 18 hours.