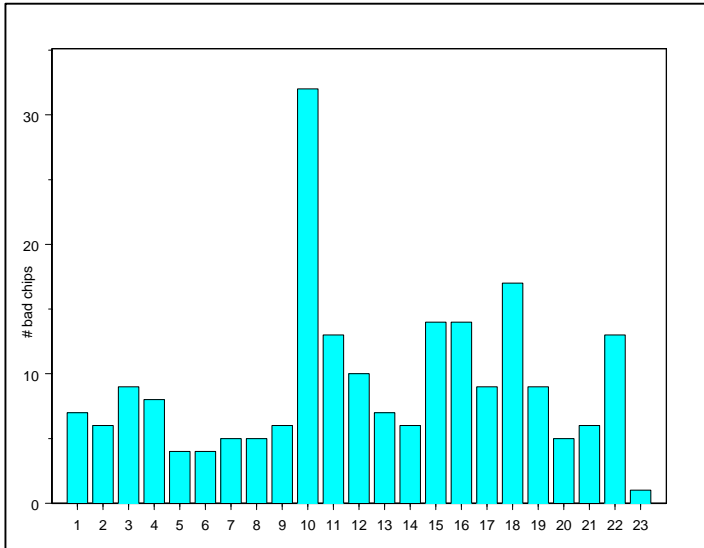


Yield of wafer lots

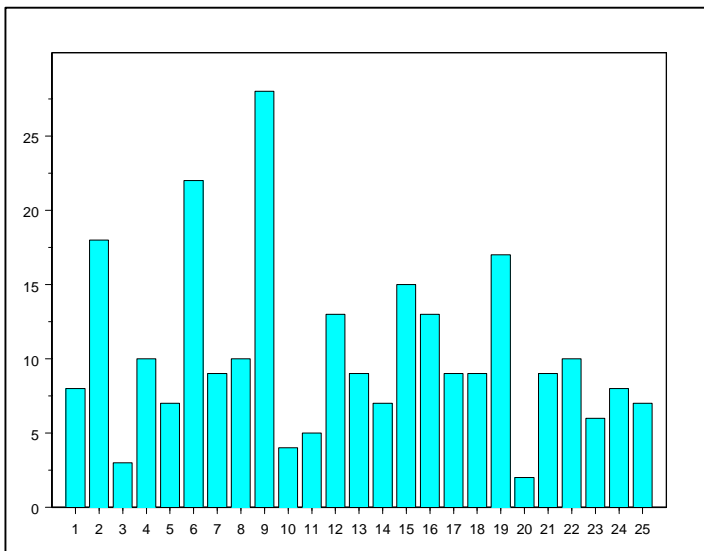
Marcus Ziegler UCSC 8/15/2003



GTFE LOT # 1

Wafers 23
Tested chips 4163
Bad chips 210

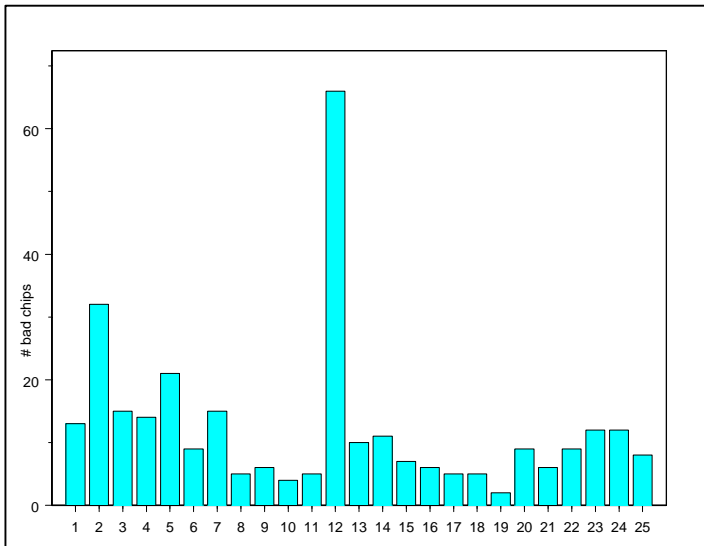
Yield 95.0%



GTFE LOT # 2

Wafers 25
Tested chips 4525
Bad chips 258

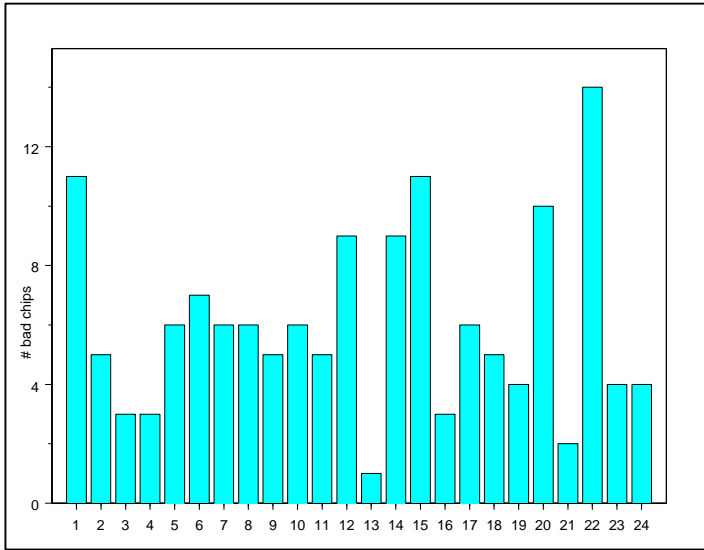
Yield 94.3%



GTFE LOT # 3

Wafers 25
Tested chips 4525
Bad chips 307

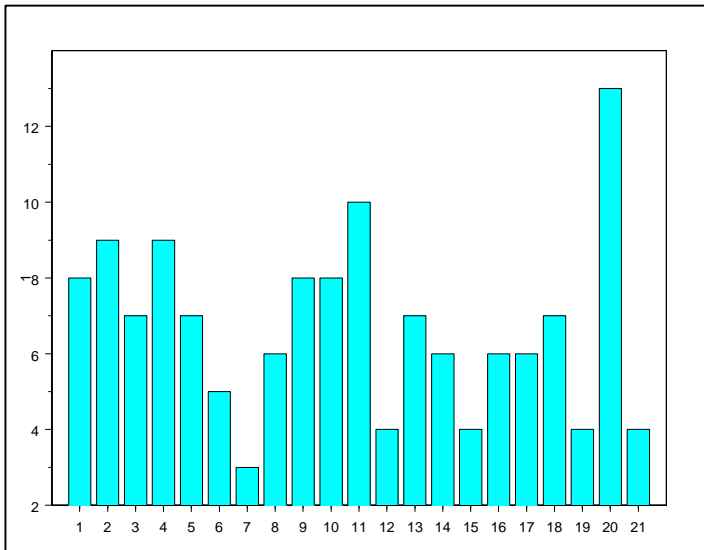
Yield 93.2%



GTFE LOT # 4

Wafers 24
 # Tested chips 4344
 # Bad chips 145

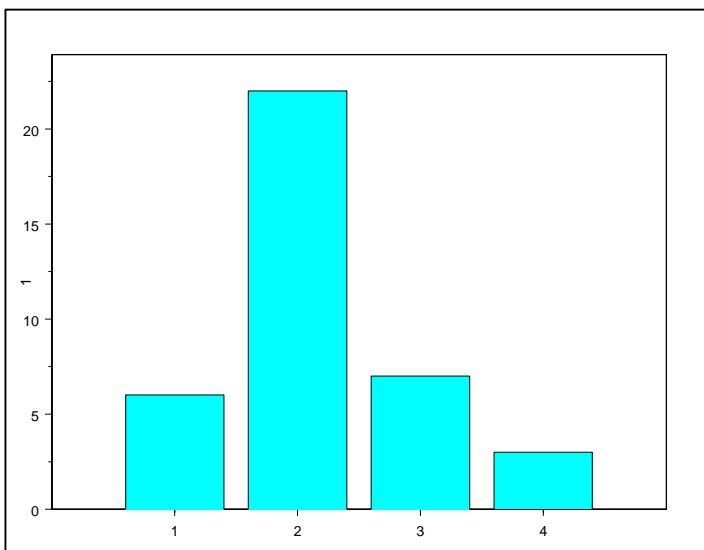
Yield 96.7%



GTFE LOT # 5

Wafers 21
 # Tested chips 3801
 # Bad chips 141

Yield 96.3%



GTFE LOT # 6

Wafers 4
 # Tested chips 724
 # Bad chips 38

Yield 94.8%

Total # Wafers 122
 Total # Chips 22082
 Total # Bad chips 1099

Yield 95.0 %