

 <h2 style="text-align: center;">GLAST Mission Interface Change Notice (ICN)</h2>		ICN No. 064 draft
		SHEET 1 OF 3
ORIGINATOR: Rich Bielawski	PHONE: 650-926-5190	DATE: 6-16-04
CHANGE TITLE: Offset Resistors for SIU_RESET		ORG: LAT
DOCUMENT NUMBER	TITLE	VERSION
1196-EI-Y46311-000	LAT-SC ICD	C
<b>REASON FOR CHANGE:</b> If there is more than 10mV of differential noise on the SIU_RESET digital control line, the LAT UTMC flight LVDS receiver may see the noise as a valid signal and cause the SIU CPU to reset.		
<b>PROPOSED CHANGE:</b> Add offset resistors to the positive and negative legs of the LAT UTMC flight LVDS receivers used on the SIU_RESET digital control lines. See attached page for implementation. The resistor values chosen provide 50mV of differential noise tolerance.		
<b>INSTRUMENT IMPACT</b> <input type="checkbox"/> Cost <input type="checkbox"/> Schedule	None.	
<b>SPACECRAFT IMPACT</b> <input type="checkbox"/> Cost <input type="checkbox"/> Schedule		
ORIGINATOR SIGNATURE:		
APPROVAL SIGNATURES:		
Spectrum Instrument Interface Lead – Tim Morse		Spectrum Program Manager – Al Lepore
 Instrument Systems Engineer – Dick Horn		 Instrument Program Manager – Lowell Klaisner

LCB

