GLAST Mission
Interface Change Notice (ICN)

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DATE: 6-16-04

CHANGE TITLE: Offset Resistors for SIU_RESET
ORG: LAT

DOCUMENT NUMBER | TITLE | VERSION
1196-EI-Y46311-000 | LAT-SC ICD | C

REASON FOR CHANGE:
If there is more than 10mV of differential noise on the SIU_RESET digital control line, the LAT UTM C flight LVDS receiver may see the noise as a valid signal and cause the SIU CPU to reset.

PROPOSED CHANGE:
Add offset resistors to the positive and negative legs of the LAT UTM C flight LVDS receivers used on the SIU_RESET digital control lines. See attached page for implementation. The resistor values chosen provide 50mV of differential noise tolerance.

INSTRUMENT IMPACT
☐ Cost
☐ Schedule

SPACECRAFT IMPACT
☐ Cost
☐ Schedule

ORIGINATOR SIGNATURE:

APPROVAL SIGNATURES:

Spectrum Instrument Interface Lead – Tim Morse
Spectrum Program Manager – Al Lepore

Instrument Systems Engineer – Dick Horn
Instrument Program Manager – Lowell Klaisner

0000-EN-O27524
24 SEPTEMBER 2001
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