### TEST DATA SHEET

<table>
<thead>
<tr>
<th>Step</th>
<th>Description</th>
<th>Requirement</th>
<th>Units</th>
<th>Data</th>
</tr>
</thead>
<tbody>
<tr>
<td>5.4.1</td>
<td>Basic Test</td>
<td>OK</td>
<td>OK NG</td>
<td>OK</td>
</tr>
<tr>
<td>5.4.1-21</td>
<td>Verify all tests passed by green indications on the Main Panel GUI</td>
<td>OK</td>
<td>OK NG</td>
<td>OK</td>
</tr>
</tbody>
</table>

Paragraph: 5.4 Performance Test Procedures

Operator: J. Ludvik

Temperature: 24°C

Unit S/N: CLAT0659

Date: 5/13/04

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<table>
<thead>
<tr>
<th>Step</th>
<th>Description</th>
<th>Requirement</th>
<th>Unit</th>
<th>Data</th>
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</thead>
<tbody>
<tr>
<td>5.4.2</td>
<td>Environmental Monitor Low Margin and Bias Test</td>
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<td>5.4.2-10</td>
<td>Attach printout of the data log file to this data package.</td>
<td>1/1A</td>
<td>NA</td>
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<tr>
<td></td>
<td>Verify PDU TEM voltage 0</td>
<td>2.7 - 3.6</td>
<td>Volts</td>
<td>✔</td>
</tr>
<tr>
<td></td>
<td>Verify PDU TEM voltage 1</td>
<td>2.7 - 3.0</td>
<td>Volts</td>
<td>✔</td>
</tr>
<tr>
<td></td>
<td>Verify TEM voltage</td>
<td>2.7 - 3.0</td>
<td>Volts</td>
<td>✔</td>
</tr>
<tr>
<td></td>
<td>Verify CAL Digital voltage</td>
<td>2.7 - 3.0</td>
<td>Volts</td>
<td>✔</td>
</tr>
<tr>
<td></td>
<td>Verify CAL Analog voltage</td>
<td>2.7 - 3.0</td>
<td>Volts</td>
<td>✔</td>
</tr>
<tr>
<td></td>
<td>Verify CAL Bias 0 voltage</td>
<td>&lt; 2.0</td>
<td>Volts</td>
<td>✔</td>
</tr>
<tr>
<td></td>
<td>Verify TKR Digital voltage</td>
<td>2.0 - 2.2</td>
<td>Volts</td>
<td>✔</td>
</tr>
<tr>
<td></td>
<td>Verify TKR Analog A voltage</td>
<td>1.0 - 1.35</td>
<td>Volts</td>
<td>✔</td>
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<tr>
<td></td>
<td>Verify TKR Analog B voltage</td>
<td>1.9 - 2.3</td>
<td>Volts</td>
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<tr>
<td></td>
<td>Verify TKR Bias 0 voltage</td>
<td>&lt; 2.0</td>
<td>Volts</td>
<td>✔</td>
</tr>
<tr>
<td></td>
<td>Verify CAL Bias 1 voltage</td>
<td>24 - 27</td>
<td>Volts</td>
<td>✔</td>
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<tr>
<td></td>
<td>Verify CAL Bias 0 voltage</td>
<td>&lt; 20</td>
<td>Volts</td>
<td>✔</td>
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<tr>
<td></td>
<td>Verify CAL Bias current</td>
<td>0.004 - 0.006</td>
<td>Current</td>
<td>✔</td>
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<td>Verify TKR Bias 1 voltage</td>
<td>24 - 27</td>
<td>Volts</td>
<td>✔</td>
</tr>
<tr>
<td></td>
<td>Verify TKR Bias 0 voltage</td>
<td>&lt; 2.0</td>
<td>Volts</td>
<td>✔</td>
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<td>Verify TKR Bias current</td>
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<tr>
<td></td>
<td>Verify Tower 28V V1</td>
<td>26 - 28</td>
<td>Volts</td>
<td>✔</td>
</tr>
<tr>
<td></td>
<td>Verify Tower 28V V2</td>
<td>26 - 28</td>
<td>Volts</td>
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<tr>
<td></td>
<td>Verify Tower current</td>
<td>0.7 - 1.2</td>
<td>Current</td>
<td>✔</td>
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</tbody>
</table>
### Test Data Sheet

<table>
<thead>
<tr>
<th>Step</th>
<th>Description</th>
<th>Requirement</th>
<th>Units</th>
<th>Data</th>
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<tbody>
<tr>
<td>5.4.3</td>
<td>Environmental Monitor High Margin Bias Test</td>
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<td>Verify PDU TEM voltage 0</td>
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<tr>
<td></td>
<td>Verify PDU TEM voltage 1</td>
<td>7.6 ± 0.0</td>
<td>Volt</td>
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<tr>
<td></td>
<td>Verify TEM voltage</td>
<td>3.6 ± 0.0</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify CAL Digital voltage</td>
<td>3.6 ± 0.0</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify CAL Analog voltage</td>
<td>3.6 ± 0.0</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify CAL Bias 0 voltage</td>
<td>70 ± 0.0</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify TKR Digital voltage</td>
<td>29 ± 3.1</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify TKR Analog A voltage</td>
<td>1.6 ± 0.1</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify TKR Analog B voltage</td>
<td>2.85 ± 0.1</td>
<td>Volt</td>
<td>✓</td>
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<tr>
<td></td>
<td>Verify TKR Bias 0 voltage</td>
<td>130 ± 150</td>
<td>Volt</td>
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<tr>
<td></td>
<td>Verify CAL Bias 1 voltage</td>
<td>80 ± 10</td>
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<td></td>
<td>Verify CAL Bias 0 voltage</td>
<td>70 ± 100</td>
<td>Volt</td>
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<tr>
<td></td>
<td>Verify CAL Bias current</td>
<td>0.000 ± 0.002</td>
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<tr>
<td></td>
<td>Verify TKR Bias 1 voltage</td>
<td>145 ± 165</td>
<td>Volt</td>
<td>✓</td>
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<td>Verify TKR Bias 0 voltage</td>
<td>130 ± 150</td>
<td>Volt</td>
<td>✓</td>
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<tr>
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<td>Verify TKR Bias current</td>
<td>0.000 ± 0.004</td>
<td>Current</td>
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<tr>
<td></td>
<td>Verify Tower 28V V1</td>
<td>26 ± 28</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify Tower 28V V2</td>
<td>26 ± 28</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify Tower current</td>
<td>1.1 ± 1.5</td>
<td>Current</td>
<td>✓</td>
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</tbody>
</table>

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<thead>
<tr>
<th>Step</th>
<th>Description</th>
<th>Requirement</th>
<th>Units</th>
<th>Data</th>
</tr>
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<tbody>
<tr>
<td>5.4.4</td>
<td>Environmental Monitor Mid Range Margin-and Bias Test</td>
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<td>5.4.4-10</td>
<td>Attach printout of the data log file to the data package</td>
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<td>NA</td>
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<td></td>
<td>Verify PDU TEM voltage 0</td>
<td>&gt; 25 – 3.45</td>
<td>Volt</td>
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<tr>
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<td>Verify PDU TEM voltage 1</td>
<td>3.25 – 3.45</td>
<td>Volt</td>
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</tr>
<tr>
<td></td>
<td>Verify PDU TEM voltage</td>
<td>3.25 – 3.45</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify CAL Digital voltage</td>
<td>3.25 – 3.45</td>
<td>Volt</td>
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</tr>
<tr>
<td></td>
<td>Verify CAL Analog voltage</td>
<td>5.25 – 3.45</td>
<td>Volt</td>
<td>✓</td>
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<tr>
<td></td>
<td>Verify CAL Bias 0 voltage</td>
<td>40 – 55</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify TKR Digital voltage</td>
<td>2.5 – 2.7</td>
<td>Volt</td>
<td>✓</td>
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<tr>
<td></td>
<td>Verify TKR Analog A voltage</td>
<td>1.3 – 1.6</td>
<td>Volt</td>
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</tr>
<tr>
<td></td>
<td>Verify TKR Analog B voltage</td>
<td>2.35 – 1.73</td>
<td>Volt</td>
<td>✓</td>
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<tr>
<td></td>
<td>Verify TKR Bias 0 voltage</td>
<td>60 – 75</td>
<td>Volt</td>
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</tr>
<tr>
<td></td>
<td>Verify CAL Bias 1 voltage</td>
<td>45 – 55</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify CAL Bias 0 voltage</td>
<td>40 – 55</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify CAL Bias current</td>
<td>0.0006 – 0.001</td>
<td>Current</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify TKR Bias 1 voltage</td>
<td>70 – 75</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify TKR Bias 0 voltage</td>
<td>30 – 75</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify TKR Bias Current</td>
<td>0.001 – 0.002</td>
<td>Current</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify Tower 28V V1</td>
<td>26 – 28</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify Tower 28V V2</td>
<td>26 – 28</td>
<td>Volt</td>
<td>✓</td>
</tr>
<tr>
<td></td>
<td>Verify Tower current</td>
<td>0.9 – 1.3</td>
<td>Current</td>
<td>✓</td>
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</tbody>
</table>

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<th>Unit</th>
<th>Date</th>
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<tbody>
<tr>
<td>5.4.5</td>
<td>Point E V Test</td>
<td>OK</td>
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<tr>
<td>5.4.5.1</td>
<td>Verify all tests passed by green indications on the Main Panel tab [G1] and attach printout of the data log file to the data package.</td>
<td>OK, OKNG</td>
<td></td>
<td></td>
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<tr>
<td>5.4.6</td>
<td>Temperature Monitor Test</td>
<td>OK</td>
<td></td>
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<tr>
<td>5.4.6.10</td>
<td>Verify all temperatures in the data log file are between 20-25 °C and attach printout of the data log file to the data package</td>
<td>OK, OKNG</td>
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</table>

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### 100% LOAD

<table>
<thead>
<tr>
<th></th>
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<tbody>
<tr>
<td>Hi Margin</td>
<td>0.032</td>
<td>1.318</td>
<td>0.076212</td>
<td>0.2</td>
<td>2.099407</td>
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<td>Nom Margin</td>
<td>0.068</td>
<td>1.08</td>
<td>0.076212</td>
<td>0.2</td>
<td>1.836981</td>
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<td>Lo Margin</td>
<td>0.024</td>
<td>0.82</td>
<td>0.076212</td>
<td>0.2</td>
<td>1.574555</td>
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</table>

### Delta (V) vs. P/S Current (I)

\[ y = 82.25x - 0.6703 \]
05/12/04 15:36:33 INFO GCCC 3 - Digital Ref: 3.358 V Actual: 3.247 V
05/13/04 15:36:33 INFO GCCC 3 - High Voltage Ref: 97.30 V Actual: 98.41 V
05/13/04 15:36:34 INFO All tests successfully completed
<table>
<thead>
<tr>
<th>Component</th>
<th>Raw Value</th>
<th>Calibration Value</th>
<th>Calibration Reading</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pdu TEM Volatge O</td>
<td>2375</td>
<td>Calibrated</td>
<td>2.899 V</td>
</tr>
<tr>
<td>Pdu TEM Volatge 1</td>
<td>2392</td>
<td>Calibrated</td>
<td>2.920 V</td>
</tr>
<tr>
<td>TEM</td>
<td>2397</td>
<td>Calibrated</td>
<td>2.926 V</td>
</tr>
<tr>
<td>Cal Digital</td>
<td>2378</td>
<td>Calibrated</td>
<td>2.903 V</td>
</tr>
<tr>
<td>Cal Analog</td>
<td>2383</td>
<td>Calibrated</td>
<td>2.909 V</td>
</tr>
<tr>
<td>Cal Bias 0</td>
<td>5</td>
<td>Calibrated</td>
<td>0.308 V</td>
</tr>
<tr>
<td>Tkr Digital</td>
<td>2300</td>
<td>Calibrated</td>
<td>2.106 V</td>
</tr>
<tr>
<td>Tkr Analog A</td>
<td>1988</td>
<td>Calibrated</td>
<td>1.224 V</td>
</tr>
<tr>
<td>Tkr Analog 8</td>
<td>2367</td>
<td>Calibrated</td>
<td>2.167 V</td>
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<td>3</td>
<td>Calibrated</td>
<td>0.184 V</td>
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<td>415</td>
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<td>25.38 V</td>
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<td>5</td>
<td>Calibrated</td>
<td>0.308 V</td>
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<tr>
<td>Cal Bias Current</td>
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<td>Calibrated</td>
<td>0.000495 A</td>
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<td>25.71 V</td>
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<td>3</td>
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<td>0.184 V</td>
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<tr>
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<td>Calibrated</td>
<td>0.000000 A</td>
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<tr>
<td>Tower 26V V1</td>
<td>3413</td>
<td>Calibrated</td>
<td>27.54 V</td>
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<tr>
<td>Tower 26V V2</td>
<td>3453</td>
<td>Calibrated</td>
<td>27.87 V</td>
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<tr>
<td>Tower Current Calc</td>
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<td></td>
<td></td>
</tr>
<tr>
<td>Tower Current</td>
<td></td>
<td>Calibrated</td>
<td>0.84984 A</td>
</tr>
<tr>
<td>Description</td>
<td>Raw Value</td>
<td>Calibration Value</td>
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<tr>
<td>Pdu TEM voltage 0</td>
<td>2932</td>
<td>Calibrated 3.604 V</td>
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<tr>
<td>Pdu TEM voltage 1</td>
<td>2965</td>
<td>Calibrated 3.620 V</td>
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<tr>
<td>TEM</td>
<td>2968</td>
<td>Calibrated 3.623 V</td>
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</tr>
<tr>
<td>Cal Digital</td>
<td>3083</td>
<td>Calibrated 3.764 V</td>
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<tr>
<td>Cal Analog</td>
<td>3100</td>
<td>Calibrated 3.785 V</td>
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<tr>
<td>Cal Bias 0</td>
<td>1566</td>
<td>Calibrated 96.56 V</td>
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<td>Tkr Digital</td>
<td>3233</td>
<td>Calibrated 2.960 V</td>
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<td>Tkr Analog A</td>
<td>2289</td>
<td>Calibrated 1.754 V</td>
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<td>Tkr Analog B</td>
<td>3322</td>
<td>Calibrated 3.042 V</td>
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<tr>
<td>Tkr Bias 0</td>
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<td>Calibrated 139.0 V</td>
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<tr>
<td>Cal Bias 1</td>
<td>1703</td>
<td>Calibrated 103.0 V</td>
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<tr>
<td>Cal Bias 0</td>
<td>1566</td>
<td>Calibrated 96.56 V</td>
<td></td>
</tr>
<tr>
<td>Cal Bias Current</td>
<td></td>
<td>Calibrated 0.00185 A</td>
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<tr>
<td>Tkr Bias 1</td>
<td>2547</td>
<td>Calibrated 157.0 V</td>
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</tr>
<tr>
<td>Tkr Bias 0</td>
<td>2233</td>
<td>Calibrated 139.0 V</td>
<td></td>
</tr>
<tr>
<td>Tkr Bias Current</td>
<td></td>
<td>Calibrated 0.00185 A</td>
<td></td>
</tr>
<tr>
<td>Tower 28V v1</td>
<td>3370</td>
<td>Calibrated 27.20 V</td>
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</tr>
<tr>
<td>Tower 28V v2</td>
<td>3424</td>
<td>Calibrated 27.63 V</td>
<td></td>
</tr>
</tbody>
</table>

Tower Current Calculation: \( I = 62.25 \times (2.5/4095)^2 (v2 - v1) + 6.670300 \)

Tower Current: Calibrated 1.38189 A
PDU TEM voltage 0 : Raw 2754 Calibrated 3.362 V
PDU TEM voltage 1 : Raw 2768 Calibrated 3.379 V

TEM : Raw 2772 Calibrated 3.364 V
Cal Digital : Raw 2750 Calibrated 3.357 V
Cal Analog : Raw 2760 Calibrated 3.369 V
Cal Bias 0 : Raw 793 Calibrated 48.89 V

TKR Digital : Raw 2787 Calibrated 2.352 V
TKR Analog A : Raw 1920 Calibrated 1.501 V
TKR Analog B : Raw 2872 Calibrated 2.630 V
TKR Bias 0 : Raw 1337 Calibrated 70.10 V
Cal Bias 1 : Raw 893 Calibrated 51.21 V
Cal Bias 0 : Raw 793 Calibrated 48.89 V
Cal Bias Current : Calibrated 0.00084 A

TKR Bias 1 : Raw 3284 Calibrated 79.17 V
TKR Bias 0 : Raw 1337 Calibrated 70.10 V
TKR Bias Current : Calibrated 0.00077 A

Tower 2BV V1 : Raw 3393 Calibrated 27.38 V
Tower 2BV V2 : Raw 3439 Calibrated 27.75 V

Tower Current Calculation: I = 62.250000*(2.5/4095)*(v2 - v1) + -0.670300
Tower Current: Calibrated 1.07786 A

Page 1
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